



“Emotion classification and mathematical modeling of speech signal for the expression of prosody in synthetic speech”

A

THESIS

Submitted to

BHARATI VIDYAPEETH DEEMED UNIVERSITY, PUNE

For award of Degree of

DOCTOR OF PHILOSOPHY

In

Electronics Engineering

Under the Faculty of Engineering Technology

Submitted by

Mrs. Pallavi Sagar Deshpande

Under the Guidance of

Dr. J. S. Chitode

Research Centre

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Pune – 411043

December 2017

CERTIFICATE

This is to certify that, the work incorporated in the thesis entitled **“Emotion classification and mathematical modeling of speech signal for the expression of prosody in synthetic speech”** for the degree of **‘Doctor of Philosophy’** in the subject of **Electronics Engineering** under the faculty of **Engineering and Technology**, has been carried out by **Mrs. Pallavi Sagar Deshpande** in the Department of Electronics Engineering at Bharati Vidyapeeth Deemed University, Pune during the period from August 2010 to February 2017 under the guidance of **Dr. J.S.Chitode.**

Place: Pune
Date: / / 2017

Prof. Dr. A. R. Bhalerao
Principal,
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CERTIFICATION OF GUIDE

This is to certify that the work incorporated in the thesis entitled **“Emotion classification and mathematical modeling of speech signal for the expression of prosody in synthetic speech”**, for the degree of **‘Doctor of Philosophy’** in the subject of **Electronics Engineering** under the faculty of **Engineering and Technology** has been carried out by **Mrs. Pallavi Sagar Deshpande**, in the Department of Electronics Engineering, at Bharati Vidyapeeth Deemed University’s College of Engineering ,Pune, during the period from August 2010 to February 2017, under my direct supervision/guidance.

Place: Pune
Date: / / 2017

Prof. Dr. J. S.Chitode
Research Guide

DECLARATION BY THE CANDIDATE

I hereby declare that the thesis entitled “**Emotion classification and mathematical modeling of speech signal for the expression of prosody in synthetic speech**” submitted by me to the Bharati Vidyapeeth Deemed University, Pune for the degree of ‘**Doctor of Philosophy**’ (Ph.D.) in **Electronics Engineering** under the faculty of **Engineering and Technology** is original piece of work carried out by me under the supervision of **Dr. J. S. Chitode**. I further declare that it has not been submitted to this or any other university or Institution for the award of any degree or Diploma.

I also confirm that all the material which I have borrowed from other sources and incorporated in this thesis is duly acknowledged. If any material is not duly acknowledged and found incorporated in this thesis, it is entirely my responsibility. I am fully aware of the implications of any such act, which might have been committed by me advertently or inadvertently.

Place: Pune

Mrs. Pallavi Sagar Deshpande

Date:

Research Student

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Mrs. Pallavi Sagar Deshpande

Place: Pune.

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